

Accurate analysis of silicon, VLSI-technology compatible spiral inductors

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In this paper, the application of a full-wave electromagnetic simulator to the analysis of solid-state (CMOS-compatible) inductors is described. By means of the simulation, performance of actual inductors has been correctly predicted and characterization of equivalent circuit components has been carried out. Impact of some design options on the inductor performance has been discussed. Detrimental effects related to the specific technology adopted have also been evaluated and physically interpreted.

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